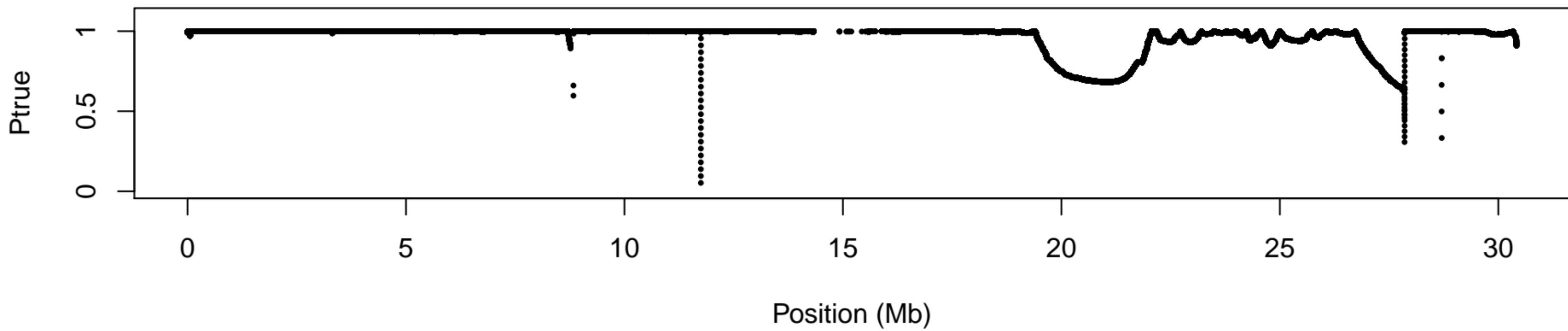
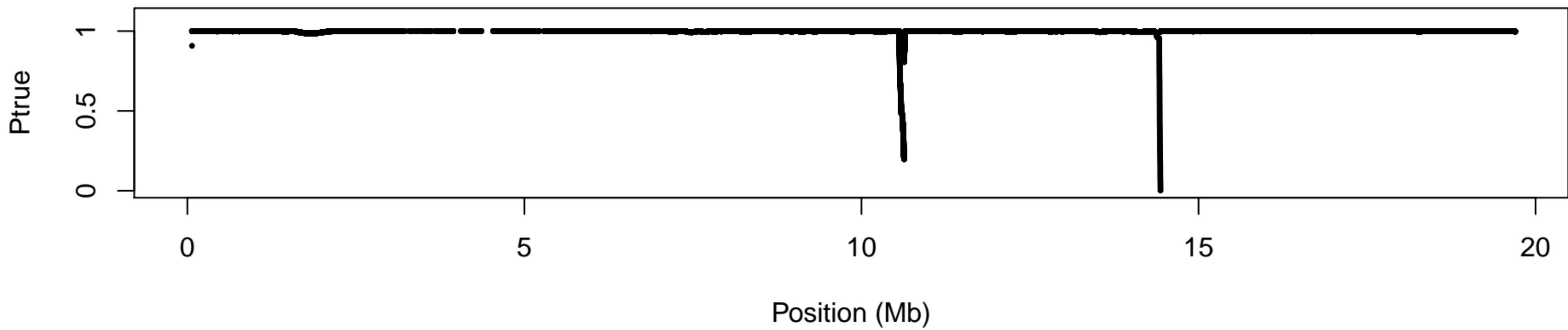


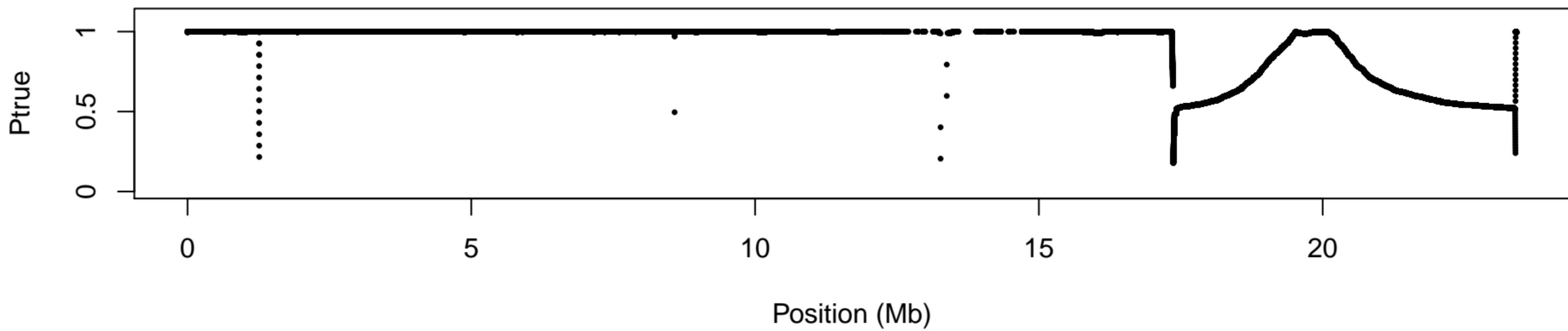
## SIM.32 – Ptrue



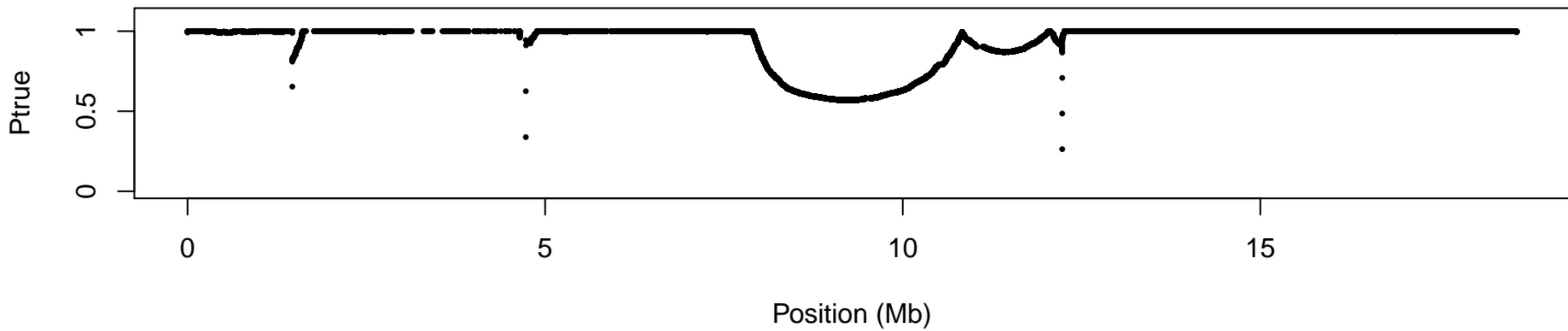
## SIM.32 – Ptrue



## SIM.32 – Ptrue



## SIM.32 – Ptrue



## SIM.32 – Ptrue

